	Application No.	Applicant(s)	
Notice of Allowability	10/796,402	OCHI, SAM SEIICH	IIRO
	Examiner	Art Unit	<u> </u>
	Zeev Kitov	2836	
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.			
1. This communication is responsive to <u>02/28/05</u> .			
2. The allowed claim(s) is/are <u>5, 6, 8, 10 - 20</u> .			
3. The drawings filed on <u>08 March 2004</u> are accepted by the Examiner.			
<ul> <li>4.  Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). <ul> <li>a)</li></ul></li></ul>			
Attachment(s)  1. ☐ Notice of References Cited (PTO-892)  2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)  3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/0: Paper No./Mail Date  4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	5. Notice of Informal Pa 6. Interview Summary ( Paper No./Mail Date 8), Examiner's Amendm 8. Examiner's Statemen 9. Other	(PTO-413), e nent/Comment	

Examiner acknowledges a submission of the amendment and arguments filed on February 28, 2005. Claims 1 - 4, 7, 9 and D are deleted; Claims 5 and 8 are amended.

Amendment has overcome rejections under 102 (b) and 103(a).

**EXAMINER'S AMENDMENT** 

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Attorney, Mr. George Yee on March 31, 2005.

Please amend Claim 8 as follows: The circuit of claim [7] <u>5</u> wherein the output of the operational amplifier couples with the first detector.

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The following is an examiner's statement of reasons for allowance:

An amended independent Claim 5 discloses, inter alia, the switch being transistor and the second detector comprises a bias voltage source; an operational amplifier having an inverting input coupled with the positive terminal and coupled with the bias source; a non-inverting input coupled with a negative terminal; and an output coupled to the gate

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of the switch, wherein the bias voltage source is coupled with the first detector. The closest references for the Claim are Goerke et al. and Johansson.

Goerke et al. disclose some of the elements of Claim 5. However, it does not disclose a second detector configured detecting noise in the power and coupled to the switch, wherein a conductivity of the switch varies responsive to the second detector.

Johansson discloses a second detector configured to detect noise. However, it does not disclose the recited above structure. The recited limitation was not found in the collected prior art of the record.

As per Claims 11 – 20, the independent Claims 11, 16 and 19 include following limitation: first circuit means for detecting a connection event wherein a connection is made between a device and a power source, the first circuit means configured to be selectively coupled to and decoupled from the power source and second circuit means responsive to the first circuit means for coupling the power to the electronic device in a gradual manner the third circuit means for detecting an overcurrent when the electronic device draws current from the power source exceeding a predetermined level; and fourth circuit means for reducing the amount of power that is applied to the device in response to the third means. A combination of the third circuit means detecting an overcurrent with the fourth circuit means reducing amount of power applied to the device acting in the environment of coupling a power source to an electronic device have not been found in the collected prior art of the record.

Allowability resides, at least in part, in the above-described limitations, which has not been disclosed in the Prior Art in a search.

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Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Zeev Kitov whose telephone number is (571) 272-2052. The examiner can normally be reached on 8:00 – 4:30. If attempts to reach examiner by telephone are unsuccessful, the examiner's supervisor, Brian Sircus can be reached on (571) 272 – 2800, Ext. 36. The fax phone number for organization where this application or proceedings is assigned is (703) 872-9306 for all communications.

Z.K. 04/01/2005

SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2800